SF SERIES

1ch isolator

Model	SF1A - 🖵 🖵 🖵 - 🖵 -
Input 01: 4 to 20mA 02: 0 to 20mA 03: 0 to 16mA 04: 2 to 10mA 05: 0 to 10mA 06: 1 to 5mA 07: 0 to 1mA Input sampling 01: 25ms 02: 125ms 03: 250ms	A DC A DC A DC A DC A DC DC DC
02: 0 to 20mA 03: 0 to 12mA	A DC 06: 0 to 1V DC A DC 07: 0 to 5V DC A DC 08: 1 to 5V DC A DC 09: 0 to 10V DC DC
Socket 1: Screw fall p (For Y term 2: For Ring te Power supply 0: 100 to 240 1: 24V AC/DC	erminal V AC

How to Order

Specify a model and input range. (e.g.) SF1A-010101-1-0 Default value

Input	4 to 20mA DC
Output	4 to 20mA DC
Input sampling period	25ms

Accessories (Sold Separately)

Name		Model	Spec.
-		RES-S01-050	50Ω±0.1%
	Ring	RES-S01-100	100Ω±0.1%
	terminal	RES-S01-200	200Ω±0.1%
Shunt		RES-S01-01K	1kΩ±0.1%
Resistor		RES-S06-050	50Ω±0.1%
	Y	RES-S06-100	100Ω±0.1%
	terminal	RES-S06-200	200Ω±0.1%
		RES-S06-01K	1kΩ±0.1%

Input Specifications

DC current [Connect a shunt resistor (sold separately) between input terminals.]

Shunt resistor
50Ω
1000
10032
200Ω
1kΩ

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Outpu DC Cur	-	ecifications	
Outp	ut	Allowable	

Output range	Allowable load resistance	Zero adjustment range	Span adjustment range
4 to 20mA DC	700 Ω or less	-5 to 5%	95 to 105%
0 to 20mA DC	700Ω or less	0 to 5%	95 to 105%
0 to 12mA DC	1.2k Ω or less	0 to 5%	95 to 105%
0 to 10mA DC	1.2k Ω or less	0 to 5%	95 to 105%
1 to 5mA DC	2.4k Ω or less	-5 to 5%	95 to 105%

DC Voltage

DO Voltage			
Output range	Allowable load resistance	Zero adjustment range	Span adjustment range
0 to 1V DC	100Ω or more	0 to 5%	95 to 105%
0 to 5V DC	500 Ω or more	0 to 5%	95 to 105%
1 to 5V DC	500Ω or more	-5 to 5%	95 to 105%
0 to 10V DC	$1k\Omega$ or more	0 to 5%	95 to 105%

Performance

Accuracy: Within $\pm 0.2\%$ of input span (at 23°C of ambient temperature)

Input sampling period: 25ms, 125ms, 250ms (Must be specified.)

Response time:

65ms (typ.)($0\rightarrow90\%$)(Input sampling period: 25ms) 225ms (typ.)($0\rightarrow90\%$)(Input sampling period: 125ms) 425ms (typ.)($0\rightarrow90\%$)(Input sampling period: 250ms)

Temperature coefficient: $\pm 0.015\%^{\circ}$ or less

Insulation resistance: $10M\Omega$ or more, at 500V DC

(Input - Output - Power) Dielectric strength: 2.0kV AC for 1 minute (Input - Output - Power)

SPEC. SHEET



SF SERIES



General Structure

Case: Flame-resistant resin Color: Light gray Front panel: Membrane sheet

- Adjustment: Using the front keypad
- Press the MODE Key. The ZERO indicator becomes lit. The unit moves to the Output ZERO adjustment mode.
- (2) Press the MODE Key in the Output ZERO adjustment mode. The SPAN indicator becomes lit. The unit moves to the Output SPAN adjustment mode.
- (3) Pressing the MODE Key returns to Step (1). If the MODE Key is pressed for approx 3 sec, or if no operation occurs for approx. 30 sec, the unit will revert to the RUN mode.

Indication:

- PWR indicator (Green):
 - Lit when power is turned ON.

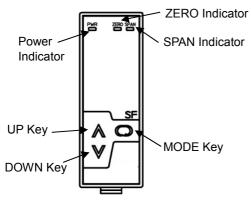
Flashes in 0.5 second cycles if non-volatile memory errors occur.

Flashes in 0.25 second cycles if input errors occur. ZERO indicator (Yellow):

Lit in the Output ZERO adjustment mode.

SPAN indicator (Yellow):

Lit in the Output SPAN adjustment mode.



Installation Specifications

Power supply: 100 to 240V AC 50/60Hz 24V AC/DC 50/60Hz Allowable voltage range: 85 to 264V AC 20 to 28V AC/DC

Power consumption: Approx. 6VA Ambient temperature: -5 to 55°C Ambient humidity: 35 to 85%RH (non-condensing) Weight: Approx. 190g (including socket) Mounting: DIN rail Dimensions: W30 x H88 x D108mm (including socket)

Attached Functions

Power failure countermeasure:

The data is backed up in non-volatile IC memory. Self diagnosis:

The CPU is monitored by a watchdog timer, and when an abnormal status is found on the CPU, the unit is switched to warm-up status turning all outputs OFF.

Environmental Specifications

RoHS directive compliance

Settings

- Function keys
 - (1) UP Key: Increases a numerical value.
 - (2) DOWN Key: Decreases a numerical value.(3) MODE Key: Switches from RUN mode to the Adjustment mode, and registers the adjustment value.

Circuit Configuration, Terminal Arrangement

